

METHOD AND APPARATUS TO IMPROVE MEMORY PERFORMANCE

Abstract

Briefly, in accordance with an embodiment of the invention, an apparatus and
5 method to improve memory performance is provided. The method may include
performing a read cycle that includes a destructive read operation and a write back
operation, wherein the destructive read operation includes reading information from a
first memory cell of a memory and wherein the write back operation includes writing the
information read from the first memory cell to a second memory cell of the memory.

10